


FACULTY PROFILE

Name of Teaching Staff			Dr. Garima Varshney				
Designation			Assistant Professor				
Department			ECE				
Date of joining the Institution:			04-AUG-2025				
Date of Birth	31-AUG-1986	College ID	F908				
Qualifications with Class/Grade			UG (B.Tech-ECE) Aligarh Muslim University, Aligarh		PG (M.Tech-VLSI Design) Delhi Technological University, Delhi	PhD Fractional-Order Signal Processing and Signal Generating Circuits	
			Percentage /CGPA 80.36% (1 st Div.)		Percentage /CGPA 80.9% (1 st Div.)	Awarded; DTU, Delhi	
Total Experience in Years (Should not be repeated)			Teaching (Excluding Research)		Industry	Research	Others
			04		-	04	-
No. of Papers Published in Journals			National		International		
			0		10		
No. of Papers Presented in Conferences			National		International		
			0		06		
Area of Specialization			Analog VLSI Design				
PhD Guide? Give field & University			Field		University		
			-		-		
No. of PhDs/Projects Guided			PhDs Ongoing	PhDs Completed	Projects at Master level		
			-	-	-		
Research Guidance (Number of Students)			No. of Papers Published in National Journals	No. of Papers Published in National Conferences	No. of Papers Published in International Journals	No. of Papers Published in International Conferences	
			-	-	-	-	
Books Published/IPRs (Books Details- Title/ISBN/Publisher/Year)							
Projects Carried Out			02 B.Tech. Projects				
Courses Taught			Diploma	Post Diploma	Under Graduate	Post Graduate	Post Graduate Diploma
			-	-	5	-	-
Patent (Filed/Granted)			02 Published				
Professional Memberships			-				
Grants Fetched / Awards			05 Research Excellence Award By DTU Delhi in 2021,2022.				
Interaction with Professional Institutions			DTU, Delhi. Dogus University, Istanbul, Turkey				
Technology Transfer			Dogus University, Istanbul, Turkey				