



AICTE-Vibrant Advocacy for Advancement and Nurturing of Indian Languages



Ajay Kumar Garg Engineering College, Ghaziabad

Department of Electronics and Communication Engineering

Two-Day Workshop On

Front-End to Back-End: Innovation and Practice in VLSI Design

Emerging Field: Semiconductors

In Hindi

Workshop Schedule

Time	Morning Session					Afternoon Session		
	09:00 AM – 10:30 AM		11:00 AM – 12:00 PM	12:00 PM – 01:00 PM		01:45 PM – 03:00 PM	03:00 PM – 04:00 PM	
Day 1 (26/9/25)	Session I Mr. Preet Yadav Head, India Innovation Ecosystem NXP Semiconductors, Noida Topic: Semiconductors -The way forward	H I G H T E A	Session II Mr. Preet Yadav Head, India Innovation Ecosystem NXP Semiconductors, Noida Topic: Driving the Future: VLSI Design in Next-Gen Automotive Mobility	Session III Dr. Ambika Prasad Assistant Professor Indian Institute of Technology Jammu Topic: Integrated Circuit Reliability and Quality: Challenges, Opportunities, and Roadmap	L U N C H	Session IV Prof. Neeta Pandey Professor DTU, Delhi Topic: SRAM design for SoC	Session V Dr. Ambika Prasad Assistant Professor Indian Institute of Technology Jammu Topic: Hardware Security Primitives for Edge AI Devices	T E A
Day 2 (27/9/25)	Session I Dr. D. Vaithiyanathan Assistant Professor National Institute of Technology Delhi Topic: ASIC/SoC Design	T E A	Session II Mr. Vivek Kumar Dikshit Senior R&D Manager Synopsys India Pvt. Ltd., Noida Topic: Design Rule Checks (DRC), Layout Versus Schematic (LVS)	Session III Mr. Vivek Kumar Dikshit Senior R&D Manager Synopsys India Pvt. Ltd., Noida Topic: Tape-out Preparation in VLSI design flow		Session IV Mr. Atul Bhargava Senior Group Manager ST Microelectronics, Noida Topic: A comprehensive study of VLSI design flow and the importance of EDA tools	Session V Mr. Atul Bhargava Senior Group Manager ST Microelectronics, Noida Topic: EDA solutions complemented by AI - New age EDA solutions and the future of design flows	

For Registration Contact : (1) Dr. Himani Garg, Professor, Department of ECE, AKGEC, Ghaziabad, Email: garghimani@akgec.ac.in, (M):+91 9650655307

(2) Dr. Amit Garg, Associate Professor, Department of ECE, AKGEC, Ghaziabad, Email: gargamit@akgec.ac.in, (M):+91 8700486355