

ADDITIONAL DOCUMENTS

2.3.1 Student centric methods, such as experiential learning, participative learning and problem-solving methodologies are used for enhancing learning experiences (20) (2023-24)



27" Km Stone, Delhi-Hapur Bypass Road, Adhyatmik Nagar, Ghaziabad - 201 009 Phones: 8744052891 to 94, 7290034976, 7290034978, Email: info@akgec.ac.in • Website: www.akgec.ac.in

AJAY KUMAR GARG ENGINEERING COLLEGE



(Affiliated to Dr. APJ Abdul Kalam Technical University, Lucknow, UP, College Code - 027)

2.3.1 Student centric methods, such as experiential learning, participative learning and problem-solving methodologies are used for enhancing learning experiences

- Various Laboratories for enhancing learning experiences
- Pictures of technical events

Software Development/ Excellence Cell



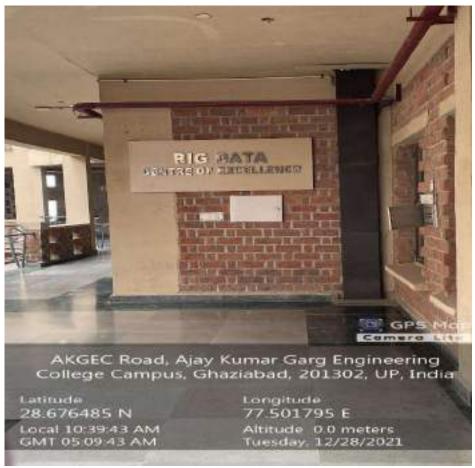




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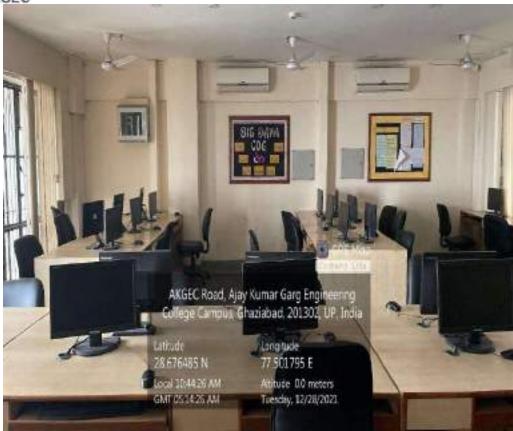
CSIT BLOCK, THIRD FLOOR, SOFTWARE DEVELOPMENT CENTER







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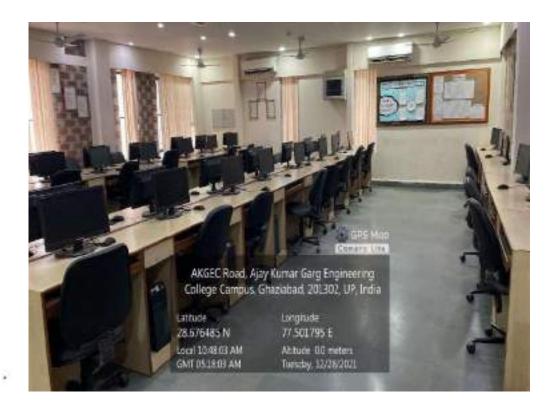
CSIT BLOCK, THIRD FLOOR, BIG DATA CENTER OF EXCELLENCE LAB





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CLOUD COMPUTING CELL

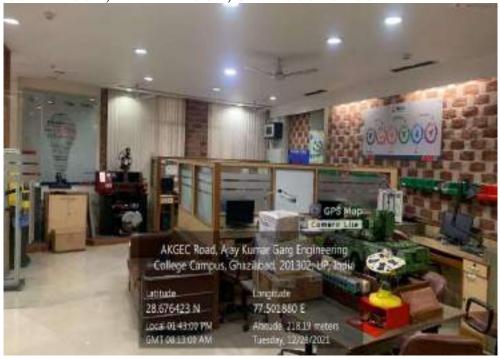




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CSIT BLOCK, THIRD FLOOR, OPEN SOURCE SOFTWARE LAB



MECHANICAL BLOCK, GROUND FLOOR, CENTER OF EXCELLENCE (FAB LAB)





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AJAY KUMAR GARG ENGINEERING COLLEGE, GHAZIABAD DEPARTMENT OF CIVIL ENGINEERING

TITAL OF EXPERIMENT (S) BEYOND SYLLABUS	Marshal mix design	Specific gravity of course aggregate	Determination of BOD.	Determination of fluoride.	instead of study of full set of structural drawing should be told to make good for constructon stage drawing of residential building, commercial building educational building.	Detailing of structuaral elements as per new carthquake Resistant design code and detailing codes for seismic zones.	To find deflections in Beam having unsymmetrical bending.	To find horizontal thrust in a three-hinged	arch and to draw influence line diagrams for horizontal Thrust end Bending moment.		impact test for coarse aggregate	crushing strength test for coarse aggregate	Retrieving soil samples and conducting SPT tests by advancing boreholes through hand-	Determination of shear strength of soil by Direct shear test.	To prepare conventional symbol chart based	on the study of different types of topographical mans	To perform fly leveling with Auto/tilting	determination of boundary layer thickness	find velocity profile over a plate	Estimation of Quantities for footing	Estimation of Quantities for Stairecase
NUMBER OF PRACTICAL BEYOND CURRICULU	2			7	2			•	*			2	3			2	0)	2			,
NUMBER OF PRACTICAL AS PER CURRICULUM	5	9		00	œ			\$	2			17	œ			11			14	u	c
Faculty Name	Mr. Bareland Americal AB	Mr. Prestrant Agarwat, Ar	0.000	Mr. Animesh Rai, AP	Mr. Priyank Srivastava, AP	11		May Assel Towns AD	Mr. Att 1948, Mr.			Mr. Prashant Agarwal, AP	Mr. Privank Srivastava, AP	•		Ms. Anubha Gupta, AP			Ms. Nikita Jaiswal, AP	Me Vehama Chulda AD	Ms. Ashama Shukid, AP
	4 1 1 1 1 1 1	I rarisportation Engineering Lab		Environmental Engineering Lab	Structural detailing lab	M		Activities and a second	Solid Mechanics Lab			Material Testing Lab	Geotechnical Engineering Lab			Surveying & Geomatics Lab			FM LAB	40.000	UEM LAD.
LAB CODE LAB NAME	100 000	KCE-951		KCE-652	KCE-653			144	KUE-452			KCE-451	KVE-552			KCE-352			KCE-353	200	KCE-553
Semester	-	5		5	5			1	2		1,000	2	>			=	8	=		***	
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from H.E. The Governor of U.P. for two consecutive years

AJAY KUMAR G. G ENGINEERING COLLEGE CAZIABAD

CSE DEPARTMENT

LIST OF EXPERIMENTS OFFERED TO STUDENTS BEYOND SYLLABUS

Vear	Semester	Lab Name	Lab Code	Faculty Name	List of Experiments offered beyond syllabus (Minimum two experiments)
				National Management of the Control o	a new
					1. Write a program in C to implement lower of riand.
		A COMPILIDATION OF A LANGE	1000 354	Pe Carobia Korman	Write a program in C to implement insertion and Deletion in Linked list.
200			100.00	4.7	3. To implement sorting using arrays.
-					4. To implement Graph Traversal.
			136 307	Mr. Athiboh money	1. Design of N-bit Carry Look ahead Adder
	#	Computer Organization Lab	Nos-306	Mr. AKINGSH VERING	2 Design N-bit Counter
2nd Year					1. Baseball best-of-5 series: Experimental probabilities
					2 Baseball Binomial Probability
		Discrete Structure & Logic Lab	KCS-353	Ms. Hamit saini	3. Expected Value Problems
					4. Baskerball: One and One
					5. Binary Relations: Influence
					1. To Write a Python program for Designing a calcutor.
		Python Programming Lab	KCS - 452	Ms. Hamit sam	Design and understand the working of pycham IDE plateform.
- 40		DATABASE MANAGEMENT SYSTEM LAB KCS - 551	KCS - 551	Mr. sandeep Yadav	 PUSQL programming Write a PUSQL block code to print the squares of numbers upto 99. Write a PUSQL block code to insert data into table CUSTOMER
_	>	av i moleculo de indravor	625 900	Mr Bradoon Gunta	1. Witte program for string marripulation.
_	Š	COMPLEX DESIGN CAS	No. o. o.c.	andro document	2. Write program to impliment SLR parser.
3rd Vear		DESIGN AND ANALYSIS OF ALGORITHM	-		1. Write a program to implement N Queen Problem using Backtracking
		LAB	NCS - 553	LV. Kajesn Prashad	2. Write a program to perform Travelling Salesman Problem
_			900	Pr Consm Gurds	1. Draw the Object Diagram of the problem.
		Software Engineering Lan	NG- 601	or conain outra	 Prepare test cases for testing the system using White box Black Box Approach.
	5	Web Technology Lab	KCS - 652	Dr. Shahank Sahu	Write program in jave to alkel rooms in the hostel using inheritance, incorporate method overloading and method overriding concept in the program.
4th Year	5	Artificial Intelligence Lab	KCS - 751	Dr. Pratima Singh	Statiscal Analysts using Numpy in Python Programming Language. Implimentation of linear repression using Python Programming Language.

Dr. Sunita Yadav Prof. CSE

Alay Kumar Garg Engg. Collegi Ghaziabad Director

AJAY KUMAR CARGINEERING COLLEGE CATABAD

CSE DEPARTMENT

LIST OF EXPERIMENTS OFFERED TO STUDENTS BEYOND SYLLABUS

Year	Semester	Lab Name	Lab Code	Faculty Name	TOTAL CHARLES AND
					1. Write a program in C to implement Tower of Hanoi.
					2. Write a program in C to implement Insertion and Defetion in United list.
		DATA STRUCTURE USING C	KCS - 351	Dr. Sachin Kumar	The state of the s
					3. To implement sorting using arrays.
					4. To implement Graph Traversal.
	0				1. Design of N-bit Carry Lock ahead Adder
	į	Computer Organization Lab	KCS - 352	Mr. Akhilesh verma	2. Design N-bit Counter
and Vanc					1, Baseball best-of-5 series: Experimental probabilities
DEL DE					2. Baseball: Binomial Probability
		Discrete Structure & Logic Lab	KCS - 353	Ms. Hamil saini	3. Expected Value Problems
					4. Basketball: One and One
					5. Binary Relations: Influence
			1000		1. To Write a Python program for Designing a calculor.
		Python Programming Lab	KCS - 452	Ms. Hamit saini	Design and understand the working of pycharm IDE plateform.
		DATABASE MANAGEMENT SYSTEM LAB KCS - 551	KCS - 551	Mr. sandesp Yadav	 PLSQL programming Write a PL/SQL block code to print the squares of numbers upto 99. Write a PL/SQL block code to insert data into table CUSTOMER
	3			4	1. Write program for string manipulation.
_	>	COMPILER DESIGN LAB	KCS - 352	Mr. Pradeep Gulpa	2. White program to impliment SLR parser.
		MUTANCE AND SECURITY			1, Write a program to implement N Queen Problem using Backtracking
3rd Year	284	DESIGN AND ANALTSIS OF ALCOHOLINIA KCS - 553	KCS - 553	Dr. Rajesh Preshad	2. Write a program to perform Travelling Salesman Problem
					1. Draw the Object Diagram of the problem.
		Software Engineering Lan	KCS - 601	Dr. conam cupra	Prepare test cases for testing the system using White box/Black Box Approach.
	>	Web Technology Lab	KCS - 662	Dr. Shahank Sebu	 Write program in java to allot rooms in the hostel using inheritance. Incorporate method overloading and method overriding concept in the program.
	_				1. Statiscal Anaheis using Numpy in Python Programming Language.
4th Year	ar	Artificial Intelligence Lab	KCS-751	Dr. Pratima Singh	Implimentation of linear regression using Python Programming Language.

Dr. Sunita Yadav Prof. CSE

Director
Ajay Kumar Garg Engg. College
Ghaziabad

ALAY KUMAR GARG ENGINEERING COLLEGE GNAZIABAD ECE DEPARTMENT OF EXPERIMENTS OFFERED TO STUDENTS BIYOND SYLLAS

Lab Name Lab Name				int Code	List of Exp.	List of Experiments offered beyond syllabus (Minimum two experiments)
I/II Electronics Engineering Lab KEC351 Dr. Seema Garg	Year	Semester	Lab Name	Lab Code	COLUMN TABLES	
Electronics Engineering Lab KEC351 Mr. Alok Kumar Communication Engineering Lab KEC451 Mr. Anu Goel Communication Engineering Lab KEC451 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Microcontroller Lab KEC453 Mr. Dushyant Singh Chauhan Microcontroller Lab KEC453 Mr. Con Krishna Gupia Microcontroller Lab KEC453 Mr. Con Krishna Gupia Microcontroller Lab KEC453 Mr. Con Krishna Gupia Mgtal Communication Lab KEC453 Mr. Netraj Sharma CAD for Electronics Lab KEC453 Mr. Netraj Sharma CAD for Electronics Lab KEC453 Mr. Netraj Sharma Microware & Radar Engineering Lab KEC751D Ms. Richa Sharma	1	1.74	de l'action de l'action de la contraction de la	VECTOR OF VECTOR 10	Dr. Seema Gare	1. Study of series and parallel clipper circuit.
Electronics Devices Lab KEC351 Mr. Alok Kumar Communication Engineering KEC451 Dr. Jitender Chhabra Communication Engineering KEC451 Mis. Anu Goel Analog Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Microcontroller Lab KEC453 Ms. Salshii Mittail Microcontroller Lab KEC453 Ms. Salshii Mittail Digital Signal Processing Lab KEC453 Ms. Salshii Mittail Digital Signal Processing Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Anup Kumar Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma	1St Year	0/1	Electronics Engineering Lab	MCLISTE / NECESSE	Giorgia Carlo	 Study Opamp as an inverting and non-inverting ampetier.
Dighal System Design Lab KEC451 Ms. Anu Goel			Discharater Designer Lab	WECASE	Mr. Alok Kumar	1. Application of transistor as a switch.
Dignal System Design Lab KEC352 Dr. Jitander Chhabra Communication Engineering KEC451 Mis. Anu Goel Analog Circuits Lab KEC452 Dr. Aruna Tyagi Signal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC-551 Mr. Om Krishna Gupta Microcontroller Lab KEC-552 Ms. Salshi Mittal Microcontroller Lab KEC-553 Dr. Amit Garg Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Signal Processing Lab KEC-553 Mr. Neeraj Sharma Cab for Electronics Lab KEC-653 Mr. Neeraj Sharma Cab for Electronics Lab KEC-653 Mr. Anup Kumar Microwave & Radur Engineering Lab KEC751D Ms. Richa Sharma			ENCROPIES DEVICES LES	ACCOUNT.		 To study the characteristics of regulated DC power supply.
Digital System Design Lab KEC451 Dr. Jitender Chhabra Communication Engineering KEC451 Ms. Anu Goel Analog Circuts Lab KEC452 Dr. Anuna Tyagi Signal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuts Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuts Lab KEC453 Mr. Om Krishna Gupta Microcontrollar Lab KEC453 Ms. Sakshi Mittal Microcontrollar Lab KEC453 Dr. Annit Garg Digital Signal Processing Lab KEC453 Dr. Annit Garg Vi		-				1. Design and implement Half Adder and Full Adder using Logic gates.
Communication Engineering REC451 Ms. Anu Goel Lab Signal System Lab REC452 Dr. Aruna Tyagi Signal System Lab REC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab REC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab REC453 Mr. Dushyant Singh Chauhan Microcontrollar Lab REC453 Ms. Sakshi Mittal Microcontrollar Lab REC453 Dr. Amit Garg Digital Communication Lab REC453 Mr. Neeraj Sharma CAD for Electronics Lab REC453 Mr. Neeraj Sharma CAD for Electronics Lab REC751D Ms. Richa Sharma			Diedal Settem Design Jah	636363	Dr. Sthooder Chhahra	2. Design and Implement Half Subtractor and Pull Subtractor Using Cogn. Bares.
Communication Engineering KEC451 Ms. Anu Goel Lab KEC452 Dr. Aruna Tyagi Signal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Microprocessor & KEC453 Mr. Dushyant Singh Chauhan Microprocessor & KEC453 Mr. Dushyant Singh Chauhan Microprocessor & KEC453 Dr. Arunk Garg Digital Signal Processing Lab KEC453 Dr. Armit Garg VI Control System Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Neeraj Sharma CAD for Electronics Lab KEC453 Mr. Anup Kumar			900			 Implement a given boolean expression using Decoder/Multiplexer.
Communication Engineering REC451 Ms. Anu Goel Lab Signal System Lab REC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab REC453 Mr. Dushyant Singh Chauhan Microcontroller Lab REC-551 Mr. Om Krishna Gupia Microcontroller Lab REC-552 Ms. Sakshi Mittal V Digital Signal Processing Lab REC-553 Dr. Amit Garg Digital Signal Processing Lab REC-553 Mr. Neeraj Sharma CAD for Electronics Lab REC-652 Mr. Neeraj Sharma CAD for Electronics Lab REC-653 Mr. Anup Kumar Microwave & Radar Engineering Lab REC-751D Ms. Richa Sharma						4. Design a decade counter.
Analog Circuits Lab KEC452 Dr. Aruna Tyagi	nd rea	5	Communication Engineering	COLUMN ACT	100000000000000000000000000000000000000	 Simulation of different Line Coding Schemes using MATLAB.
N Analog Circuts Lab KEC452 Dr. Aruna Tyagi Sgnal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC.551 Mr. Om Krishna Gupta Microcontroller Lab KEC.552 Ms. Sakshi Mittail Nicrocontroller Lab KEC.553 Dr. Amit Garg Digital Signal Processing Lab KEC.553 Dr. Amit Garg VI Control System Lab KEC.553 Mr. Neeraj Sharma CAD for Electronics Lab KEC.653 B Mr. Arrup Kumar VII Microwave & Radar Engineering Lab KEC.551D Ms. Richa Sharma			Lish	VEC-451	Ms. And Goel	To generate Pre-Emphasis & De-Emphasis waves using MATLAB.
Signal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC453 Mr. Dushyant Singh Chauhan Microcontroller Lab KEC-552 Ms. Sakshi Mittail Microcontroller Lab KEC-553 Dr. Amit Garg Digital Signal Processing Lab KEC-553 Dr. Amit Garg Mr. Neeraj Sharma Cab for Electronics Lab KEC-653 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 Mr. Neeraj Sharma Ms. Richa Sharma		2	Annual Control	Contract of the Contract of th		 To study Opamp as an inverting and non-inverting ampilifier
Signal System Lab KEC453 Mr. Dushyant Singh Chauhan Integrated Circuits Lab KEC-551 Mr. Om Krishna Gupta Micropensessor & KEC-552 Ms. Sakshi Mittal Microcontroller Lab KEC-553 Ms. Sakshi Mittal Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Communication Lab KEC-553 Mr. Neeraj Sharma Cab for Electronics Lab KEC-653 Mr. Arrup Kumar Vii Microwave & Radar Engineering Lab KEC7510 Ms. Richa Sharma			Anang Circuis (30	NEL-432		To determine gain and bandwidth of CE amplifier from its frequency response
Integrated Circuits Lab KEC-551 Mr. Om Krishna Gupta Will Microwave & Radar Engineering Lab KEC-558 Mr. Neeraj Sharma VI Control System Lab KEC-653 Dr. Amit Garg CAD for Electronics Lab KEC-653 Mr. Neeraj Sharma VII Microwave & Radar Engineering Lab KEC-550 Ms. Richa Sharma			Const Contact lab	NEC. ACS	Bushasan Claute Chamban	1. Verification of sampling theorem using MATLAB
Integrated Circuits Lab KEC.551 Mr. Om Krishna Gupta Microperocessor & KEC.552 Ms. Sakshi Mittal Microcontroller Lab KEC.553 Ms. Sakshi Mittal Digital Signal Processing Lab KEC.553 Dr. Amit Garg Digital Communication Lab KEC.653 Dr. Abhishek Joshi Vi			officer observer care	Appende	MI. Dusingant singn Chaunan	Implementation of decimation and interpolation processes using MATLAB
Witcherson & KEC-552 Ms. Sakshi Mittal Microcontroller Lab KEC-553 Ms. Sakshi Mittal Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Communication Lab KEC-553 Dr. Amit Garg Vi Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 Mr. Neeraj Sharma VII Microwave & Radar Engineering Lab KEC-51D Ms. Richa Sharma			Integrated Coults Lab	KEC.051	Mr Om Krishna Gunta	 To study and design adder/subtractor circuit using op-amp.
Witcroprocessor & KEC-552 Ms. Sakshi Mittail Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Communication Lab KEC-651 Dr. Abhishek Joshi VI Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 Mr. Neeraj Sharma VII Microwave & Radar Engineering Lab KEC-551D Ms. Richa Sharma				200.000	and an arrange order	To study and design integrator and differentiator using op-amp.
V Microcontroller Lab Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Communication Lab KEC-553 Dr. Amit Garg VI Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 B Mr. Anup Kumar VII Microwave & Radar Engineering Lab KEC-51D Ms. Richa Sharma			Microprocessor &	KEC-552	Ms. Sakshi Mittal	 To study 8085 processor and various commands for its operation.
Digital Signal Processing Lab KEC-553 Dr. Amit Garg Digital Communication Lab KEC-651 Dr. Abhishek Joshi VI Control System Lab KEC-652 Mr. Neuraj Sharma CAD for Electronics Lab KEC-653 B Mr. Anup Kumar VII Microwave & Radar Engineering Lab KEC-51D Ms. Richa Sharma		>	MICROCONTRONAL LAD			To interface 8255 programmable peripheral interface and verify its operation.
Digital Communication Lab KEC651 Dr. Abhishek Joshi Vi Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 B Mr. Anup Kumar VII Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma			Distant Cleans December 1-4	5		1. With the help of Fourier series make a square wave from sine wave and cosine wave.
Vi Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 B Mr. Anup Kumar VII Microwave & Redar Engineering Lab KEC-51D Ms. Richa Sharma	3rd Yes		den Sussannia milita milita	VEC-038	Ur. Amit barg	Generate an Amplitude Modulation having side lobe frequencies 1200 Hz and 800 Hz.Observe and verify the theoretical FFT characteristics with the observed ones.
VI Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 8 Mr. Anup Kumar VII Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma			Digital Communication Lab	KEC651	Dr. Abhishek Joshi	1. To generate the constellation diagram and plot symbol error probability for the 4-PAM
VI Control System Lab KEC-652 Mr. Neeraj Sharma CAD for Electronics Lab KEC-653 8 Mr. Anup Kumar VII Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma			1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	100000000000000000000000000000000000000	The second second	 To Study the PCM and plot Message signal, sampled signal, quantized signal, and digital sign.
CAD for Electronics Lab KEC-653 8 Mr. Anup Kumar VII Microwave & Redar Engineering Lab KEC751D Ms. Richa Sharma		5	Control System Lab	KEC-652	Mr. Neeraj Sharma	 Determine the block diagram reduction of a transfer function using simulink
VII Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma			CAO for Chartenation I sh			1. A grapher of CAOC immediate function.
VIII Microwave & Radar Engineering Lab KEC751D Ms. Richa Sharma			CAD TOT ENGINEES LAD	VEC-653 B	Mr. Anup Kumar	2. Design the Layout of CMOS inverter.
	4th Yea		Microwave & Radar Engineering Lab	KEC751D	Ms. Richa Sharma	1. VSWR measurement with open and short circuit.

Dr. P. K. Chopra Prof. ECE

Ajay Kumar Garg Engg. College Ghaziabad

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MAR GARG ENGINEERING COLLEGE GHAZIABAD

COLLEGE

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. No.	Yes		m	Lab Name	Lab Code	Faculty Name	UDENTS BEYOND SYLLABUS Ust of Experiments offered beyond syllabus (Minimum two experiments)
1	-			Computer Organization Lab	KC8 352	Mr. Pankaj Singh	Implementation and Verify logic gates AND, OR, NOT, XOR:
5						PRODUCTION OF THE PRODUCTION O	Implementation and Verify Universal gates NAND and NCR
-	1	1		Ciscosta Structura d Thursday			Apply Kruskal and Prin's algorithm in a graph
_	-	1		Discrete Structures & Theory of I	NU6 263	Ms. Nandta Goyal	Write a program to solve recourence relation
_	1						Write a program to apply Pigeonhole principle
				Deta Structures using a lab	KCS 351	Mr. Puneel Kurnar Goya	Source Matrix Deduces
-	+						AVI. Tree: Implementation of Create, Insert/Rotale, Traverse operations.
-	-						Write a program to implement Optimal Page replacemt algorithms
_	-			Operating System Lab	KC8 451	Mr. Parcham	Write a program to Semaphore synchronization problems
9	Yea Yea						3. Write a program to implement Second Chance algorithms
				Python Lieb	KNC 453	Mr. Lucknegh	Write a program to create radio button (male, female and transgender) and a label. Default soluction should be on female the label must display the current soluction made by user
_	4						 Write a program to create a window that disappear automatically after 5 second
_	4						3. Write a program to create an arithmetic calculator using kiniar
_	-			Web Designing lab	KIT 451	Mr. Santosh	To design a registration form of IRCTC railway ticket booking.
-	-						To design a calculator using java script.
	-						To perform Addition and Subtraction using 6085.
	ŧ.			Micro Processor Lab	KCS452	Ms Sulekta	To sort an array using 8066.
	-						To perform Multiplication using 6060.
-	-	-	-				To calculate square root of 8 bit number using 8065.
-	-			Database management to	100,000		Creating Entity-Relationship Diagram using case tools.
				Dotabase management lab	kos 551	Mr. Lucinesh	Write a PL/SQL block code to print the squares of numbers unto the Write a PL/SQL block code to insert data into table
				Doolgn And Analysis of	kes 553	Mr. Biografia	Program for Linear Search, Sinary Search
	-			algorithm lab	.000		Program for BFS and DFS on a graph represented using Asjacency list. Write an XXVL program providing the book delate and depty the
				Web Technology lab	KIT 551	Mr. Anmol Jain	document in Internet Extense: Write an XML program for creation of DTD , which specifies set of rules.
	Î						Wheless configuration using packet tracer along with server configuration for DHCP, HTTP, DNS, FTP
	1			Computer Network Lab	KCS 653	Me Tehra Mazundor	Router configuration to communicate between different
							Implementation of Cryptographic algorithms (RSA, DES)
							PYTORCH_Neural_Historic_MNIST_HandWrittenDigits_with_validation— GazzwChaudhary
				Data Analytica Lab	KIT 651	ansha ShuklaGaurar Cha	KUTIAS_CNN_MHST_HandWiller/Digits_with_validatonGaussy Cheachury
26	1						To perform dimensionality reduction operation using PCA for Houses Data Set.
				Software Engineering Lab	KC8861	Dr. Shivani Aggranii	Write a program of Heisterd Analysis of Software Science
							Write a program for function point analysis of cost estimation.
				Dryplography and retwork secur	KIT 781	Ms. Shilpi Gupta	Write a program to implement Message authentication code Write a program to implement flestal cipher structure
							development of Al based Projects
				35400000		0.000000	development of Blockchain based Projects
	4			Project Lab	KIT 753	Dr. Ruchi Gupra	development of IoT Based Pprojects
	1						Python Programming
							development of Al based Projects
		IV					development of Stockshain based Projects

Director
Alay Kumar Gery Bogg, College
Ghezabad

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AJAY KUMAR GARG ENGINEERING COLLEGE GHAZIABAD EN PERBENTS OFFERED TO STIMENTS REVOND SYLLABIS

			TST /	OF EXPERIMEN	LIST OF EXPERIMENTS OFFERED TO STUDENTS BEYOND SYLLABUS	VTS BEYOND SYLLABUS
ó	Year	Semester	Lab Name	Lnb Code	Faculty Name	List of Experiments offered beyond syllabus (Minimum two experiments)
			Bart Handard Towns	KEE151P/	Mr. Manisoni Sharman	 Determine the time current characteristics of given MCB (6 Amp).
	1st Year	11/11	Basic Electrical Engineering Lan	KEE251P	ME, Navyou Statms	Calibration of Single Phase Energy meter.
			Electrical Measurement &	100	4	1. Signal conditioning of analog signal using Lab VIEW/MATLAB.
		1	Measurement Lab	NEE-335	Ms. Nidhi Singn	2. Measurement of power using CT and PT.
100			The state of West State of St.	VIII 263	The state of	. Measurement of form factor of a rectified sine wave.
100			Electrical workshop Lab	NEE-333	Dr. J. G. Tadav	To study of different kinds of power Insulators.
100						 To perform the transient response of RL circuit.
oc.	2nd Year		Network Ananlysis and Synthesis Lab	KEC-353	Mr. Parveen Dhull	 Verification of parameters properties in interconnection of 2, two port networks in cascade interconnection.
6		2				 Determination of frequency response of a Twin – T-notch filter.
01		:	Circuit Simulation Lab	KEE-451	Mr. Arun Kumar Maurya	 To determine attenuation characteristics of a low pass / high pass active filters.
Ξ			A CO.	1000	Mr. Ritesh Sharma	1. To study the construction and working of four-point DC machine starter.
12			Execution Machines - 1 Lab	NEE-432	Dr. Sarika Kalra	2. To study the torque-speed characteristic of Brushless DC motor.
-		L	A.11.	100 000	14. American Manager	1. Calculation of ground clearance of transmission line.
14			rower System - 1 Lab	NEE-931	MIT. AND AUTHE MELLY	2. Calculate the parameters for underground cable
15			4 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	200	Me Manual Mines	1. To Study PC Based Flow Control using variable speed pump.
16		>	Control System Lab	NGE-237	(VIS. LYBPAL PSIRIAL	To Study PC Based level Control using variable speed pump.
16			A LIN CONTRACTOR OF THE	V DE 663	Mr. Ritesh Sharma	 To Study the speed Control of BLDC motor using PWM method.
1			CACHING MACHING - II 140	Processos.	Dr. Sarika Kalna	2. To study IGBT controlled AC motor VVVF Drive.
99						 Determine the time current characteristics of the given MCB (6 Amp).
0	3rd Year		Power System - II Lab	KEE-651	Dr. Dibya Bharti	2. Perform load flow analysis on a 3- Bus System using G-S (Gauss Seidel) method using MATLAB
93						 To generate a square wave of a given frequency using 8253/54 IC.
100.5		N	Microprocessor & Microcontroller Lab	KEE-652	Mr. Neeraj Gupta	To perform microprocessor based stepper motor operation through 8085 / 8086 kit.
T						3. To obtain interfacing of DMA controller with 8085 / 8086 microprocessor.

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					 Four quadrant operation of DC motor using single phase dual converter trainer.
		Power Electronics Lab	KEE-653	Dr. Vani Bhargavu	2. Speed control of three phase induction motor using three phase AC voltage controller.
			National Control		 Logic implementation for traffic control Application.
4th Year	III	Industrial Automation & PLC Lab	KEN-751	Mr. Arun Kumar Maurya	Logic implementation for Bottle filling Application.

Dr. Hemant Abuja HeD (EN)

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CE- List of Experiments beyond syllabus

ENT (S) BEYOND			se aggregate	2	ó	et of structural o make good for g of residential ding educational	elements as per new ign code and ic zones.	sam having	in a three-hinged ce line diagrams for anding moment.	ggregate	r coarse aggregate
MANAGE EXPERIMENT (S) BEYOND	SYLLABUS	Marshal mix design	Specific gravity of course aggregate	Determination of BOD.	Determination of fluoride.	instead of study of full set of structural drawing should be told to make good for constructon stage drawing of residential building, commercial building educational building.	Detailing of structuaral elements as per new carthquake Resistant design code and detailing codes for seismic zones.	To find deflections in Beam having unsymmetrical bending.	To find horizontal thrust in a three-hinged arch and to draw influence line diagrams for horizontal Thrust end Bending moment.	impact test for coarse aggregate	crushing strength test for coarse aggregate
	PRACTICAL BEYOND CURRICULUM		N		z	2			N		2
NIMBER OF	Z		13		60			(0.	10		Ω
	Faculty Name	Mr. Prashant Agarwal, AP Mr. Animesh Rai ,			Mr. Priyank	Srivastava, AP		Mr. Atri Tyagi, AP		Mr. Prashant Agarwal, AP	
	LAB NAME	n Engineering Lab			And market and and	SECRETARIA COMPANY		Solid Mechanics Lab		Material Testing Lab	
		CODE KCE-651 T				KCE-653		KCE-452			
	Sem		5		5		>		2		2
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	NO X		-		74		m.		4	'n	

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Retrieving soil samples and conducting SF1 tests by advancing boreholes through hand- held auger. Determination of shear strength of soil by Direct shear test.	To prepare conventional symbol chart based on the study of different types of topographical maps	To perform fly leveling with Auto/tilting level.	determination of boundary layer thickness	find velocity profile over a plate	Estimation of Quantities for footing	Estimation of Quantities for Stairecase
Retrieving soil su tests by advancing held auger. Determination of Direct shear test.	To prepa on the stu topograp	To perfo	determi		Estima	2 Estima
2		•	-	7	3	
00 مدر ،	;	1		14		ın
Mr. Priyank Srivastava , AP	Ms. Anubha Gupta,	AP		Ms. Nikita Jaiswal,		Ms. Kshama Shukla, AP
Geotechnical Engineering Lab	+	Surveying & Geomatics Lab			TM LAB	QEM Lab.
KCE-552 Ger		KCE-352 S	+		KCE-353	KCE-553
>	-	=	+	s		<u> </u>
JE SE	+	2nd	1	2nd		Zwq

Dr. Rakesh Srivastava HOD, CE

Director
Alay Kumar Garg Engg. College
Ghazisbad

Director nar Gary Bagg, College Obsessibled

27" Km Stone, Delhi-Hapur Bypass Road, Adhyatmik Nagar, Ghaziabad - 201 009 ELECTRONICS AND COMMUNICATION ENGINEERING

DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, UTTAR PRADESH, LUCKNOW



EVALUATION SCHEME & SYLLABUS

FOR

B. TECH. FOURTH YEAR

ELECTRONICS ENGINEERING
ELECTRONICS AND COMMUNICATION ENGINEERING
ELECTRONICS AND TELECOMMUNICATION ENGINEERING

AS PER AICTE MODEL CURRICULUM

[Effective from the Session: 2021-22]



27" Km Stone, Delhi-Hapur Bypass Road, Adhyatmik Nagar, Ghaziabad - 201 009

ELECTRONICS AND COMMUNICATION ENGINEERING

AKGEC

B.Tech. VII Semester

Electronics and Communication Engineering

S.	Course Code	Course Title	P	erio	ds	Ev	valuati	ion Sche	me	En	ıd	Total	Credits
No.										Seme	ster		
			L	T	P	CT	TA	Total	PS	TE	PE		
1.	KHU701/KHU702	HSMC -1 [#] /HSMC-2 [#]	3	0	0	30	20	50		100		150	3
2.	KEC-071-074	Department Elective –IV	3	0	0	30	20	50		100		150	3
3.	KEC-075-076	Department Elective –V	3	0	0	30	20	50		100		150	3
4.		Open Elective-II	3	0	0	30	20	50		100		150	3
5.	KEC-751X	Lab for Department Elective -	0	0	2				25		25	50	1
6.	KEC-752	Mini Project or Internship Assessment**	0	0	2				50			50	1
7.	KEC-753	Project I	0	0	8				150			150	4
_		MOOCs											
		(Essential for Hons. Degree)											
		Total										850	18

Course Code	Course Title
	Department Elective-IV
KEC-071	Digital Image Processing
KEC-072	VLSI Design
KEC-073	Optical Network
KEC-074	Microwave & Radar Engineering
	Department Elective-V
KEC-075	Information Theory & Coding
KEC-076	Wireless & Mobile Communication
KEC-077	Micro & Smart Systems
KEC-078	Speech Processing

Course Code **	**Elective Lab
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KEC751A	Digital	Image 1	Processing 1	Lab
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KEC751B VLSI Design Lab

Optical System and Networking Lab KEC751C

KEC751D Microwave & Radar Engineering Lab

Page 2

^{***}Students will opt one subject from the list of Department Elective-IV with its corresponding lab. i.e. if someone has opted Digital Image Processing (KEC071) from Department Elective-IV then it will be mandatory to opt the DIP Lab (KEC751A).

Note: After successful completion of 160 credits, a student shall be eligible to get Under Graduate degree in Engineering. A student will be eligible to get Under Graduate degree with Honours only, if he/she completes additional university recommended courses only (Equivalent to 20 credits; NPTEL Courses of 4 Weeks, 8 Weeks and 12 Weeks shall be of 2, 3 and 4 Credits respectively) through MOOCs. For registration to MOOCs Courses, the students shall follow NPTEL Site http://nptel.ac.in/ as per the NPTEL policy and norms. The students can register for these courses through NPTEL directly as per the course offering in Odd/Even Semesters at NPTEL. These NPTEL courses (recommended by the University) may be cleared during the B. Tech degree program (not necessary one course in each semester). After successful completion of these Moocs courses the students, shall, provide their successful completion NPTEL status/certificates to the University (COE) through their college of study only. The student shall be awarded Hons. Degree (on successful completion of MOOCS based 20 credit) only if he/she secures 7.50 or above CGPA and passed each subject of that Degree Programme in single attempt without any grace marks.

The proposed MOOC courses are common to all B.Tech. Branches as follows:

NPTEL Course (MOOCS Courses Hons. Degree)

- Algorithms for Big Data
- 2. C Programming and Assembly Language
- 3. Concepts of Thermodynamics
- 4. Data Analytics with Python
- 5. Data Science for Engineers
- Design Analysis and Algorithm
- 7. Design of Reinforced Concrete Structures
- 8. Developing Soft Skills and Personality
- 9. Emotional Intelligence
- 10. Enhancing Soft Skills and Personality
- 11. Fundamental of Welding Science and Technology
- Fundamentals of Conduction And Radiation
- 13. Google Cloud Computing Foundation Course
- 14. Introduction To Block chain Technology And Applications
- 15. Introduction To Industry 4.0 And Industrial Internet Of Things
- 16. Introduction To Internet Of Things
- 17. Introduction To Robotics
- 18. Introduction To Embedded System Design
- 19. Manufacturing Process Technology 1 & II
- 20. Matlab Programming For Numerical Computation
- 21. Mechanics of Materials
- 22. Patent Law for Engineers and Scientist
- Speaking Effectively
- 24. Structural Analysis I

2

AICTE Model Curriculum Session 2018-19 onwards Honours Degree MOOC'S (NPTEL) Courses

सत्र 2018—19 से AICTE Model Curriculum में आनर्स डिग्री के लिए MOOC'S (NPTEL) कोर्सो

सत्र 2018—19 से विश्वविद्यालय में AICTE Model Curriculum लागू किया गया है सत्र 2021—22 में पास करने वाले छात्रों को आनर्स डिग्री प्राप्त करने के लिए सामान्य पाठयचर्या के साथ—साथ ओपन सोस लर्निंग के पाठ्यकम एनं0पीं0टीं (NPTEL) से 20 केंडिट प्राप्त करने का निर्णय लिया गया था। "अभियंत्रण एवं प्रौद्योगिकी के स्नातक स्तरीय पाठ्यकम के सामान्य पाठ्यचर्या के साथ—साथ यदि किसी छात्र 20 केंडिट के अतिरिक्त विषयों का अध्ययन, कक्षा अध्यापन या ओपन सोर्स लर्निंग के मध्यम से पूर्ण किया जाता है एवं उसका सीं0जीं 0पीं 0ए 7.5 या उससे अधिक हो तथा उसके द्वारा स्नातक पाठ्यकम के सभी विषयों की परीक्षा प्रथम प्रयास में बिना किसी कृपांक के उत्तीर्ण की गयी हो, तो वह छात्र बीं 0 टेक. पाठ्यकम को आनर्स के साथ उत्तीर्ण करेगा।

शिक्षा की गुणवत्ता के लिए पूर्व निर्धारित 06 मूक्स कोर्स के साथ 18 मूक्स कोर्सों को भी सिमालित किया गया है। बीठटेकठ पाठ्यकमों में भी मूक्स विषयों की NPTEL से मैपिगं की गयी है AICTE Model Curriculum सेलेबस में बीठ टेकठ पाठ्यकमों के विश्वविद्यालय की बेक्साइट में प्रदर्शित पाठ्यचर्या से MOOC'S कोर्स का चयन प्रदर्शित सलग्न सूची से ऐच्छिक चुनते समय इस बात का अवश्य ध्यान रखें कि वह विषय छात्रों द्वारा बीठटेकठ पाठ्यकम के दौरान किसी भी रूप में अध्ययन नहीं किया जाय। MOOC'S विषयों का चयन उस विभाग के छात्र/छात्राओं द्वारा नहीं किया जायेगा जिस विभाग/विधा में MOOC'S कोर्स NPTEL से संचालित है।

(Equivalent to 20 credits; NPTEL Courses of 4 Weeks, 8 Weeks and 12 Weeks shall be of 2, 3 and 4 Credits respectively) through MOOCs. For registration to MOOCs Courses, the students shall follow NPTEL Site http://nptel.ac.in/ as per the NPTEL policy and norms. The students can register for these courses through NPTEL directly as per the course offering in Odd/Even Semesters at NPTEL. These NPTEL courses (recommended by the University) may be cleared during the B. Tech degree program (not necessary one course in each semester). After successful completion of these Moocs courses the students, shall, provide their successful completion NPTEL status/certificates to the University (COE) through their college of study only. The student shall be awarded Hons. Degree (on successful completion of MOOCS based 20 credit) only if he/she secures 7.50 or above CGPA and passed each subject of that Degree Programme in single attempt without any grace marks.

Dr



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IDEA Lab

Under the umbrella of IDEA Lab, following cells are actively operating:

- 1) Intellectual Property Rights (IPR) Cell
- 2) Technical Business Incubation (TBI) Centre
- 3) Institution Innovation Council (IIC)

Major Achievements of IDEA Lab:

- AKGEC IPR Cell manages Utility patents, Design Patents, Trademarks, and copyrights. IPR Cell has filed more than 100+ patent applications in academic session 2023-24. This year, the IPR cell has signed three MOUs with different attorneys, which has expedited the filing process and reduced costs.
- Technical Business Incubation (TBI) Centre was inaugurated on 3rd May 2024.
- Ten startups have been incubated at the Technical Business Incubation (TBI) Centre and seed funding of 10000/- INR is given to each of the ten startups.
- AKG INCUBATOR FOUNDATION was incorporated with Ministry of Corporate Affairs, Govt. of India, on 20.02.2024 under the companies act 2013.
- A successful collaboration with DAV School, Pratap Vihar, Ghaziabad, was completed under the framework of the Institution Innovation Council (IIC), Ministry of Education's Innovation Cell (MIC), and the ATAL Tinkering Lab initiative, Government of India.

Main Activities organized by IDEA Lab:

- IPR Cell celebrated World Intellectual Property Day on April 22, 2024, by organising one-day workshop on Intellectual Property Rights (IPR) to raise awareness about Intellectual Property Rights and IP Management.
- Organised an Alumni meet for the alumni of AKGEC on 31st March 2024 in Vedanta Farms
- Organised a two days Startup Conclave event on May 2nd 3rd 2024 in which around 70 startup ideas were presented by AKGEC students.
- Organised internal SIH-2023 in September 2024 in which 137 teams presented their ideas. Top 100 teams have been shortlisted and registered on SIH-2023 portal for National Level competition scheduled in December 2023.
- Organised an Industrial Visit at ATAL Incubation Centre-BIMTECH, Noida on May 29th 2024.
- Organized an online session titled "Business Model Canvas" on June 25th, 2024 to mentor the incubated startups. Ms. Vandana Sharma, Manager Innovation Hub, AKTU, Lucknow was the distinguished speaker.
- Organized a session titled "How to Plan for a Startup and Legal & Ethical Steps" on June 27th, 2024, to mentor the incubated startups. Mr. Sajal Jain, Founder, Lapras Business Consultants (LBC) | Corporate Strategy, Finance, was the distinguished speaker.
- Organised a Field Visit to attend Startup Mahakumbh Event at Pragati Maidan, New Delhi on March 19th 2024.





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