FACULTY PROFILE

Name of Teaching Staff			Dr. Pankaj Goel					
Designation			Assistant Professor				- 1C	
Department			ECE			8		3 P
Date of joining the Institution:			28-08-2023				E	
Date of Birth	03-08-1980	College ID	F825			***		. //
Qualifications with Class/Grade			B.Tech (ECE) M.Tech (CAD) Percentage /CGPA Percent		PG (Degree Name) M.Tech (VLSI Design & CAD) Percentage /CGPA 74.61% / 8.29	PhD VLSI Implementation of Signal Enhancement Techniques		
Total Experience in Vers			Teaching (Excluding Research)		Industry	Rese	arch	Others
Total Experience in Years (Should not be repeated)				earen) Years	03 Years			
		National			International			
No. of Papers Published in Journals						09		
No. of Papers Presented in Conferences			National			International		
			02			08		
Area of Specialization			VLSI Design					
PhD Guide? Give field & University			Field			University		
			No			NA		
No. of PhDs/Projects Guided			PhDs Ongoing		PhDs Completed	Projects at Master level		aster level
					4			
Research Guidance (Number of Students)			No. of Papers Published in National Journals		No. of Papers Published in National Conferences	No. of Paper Published in Internationa Journals	n d l	No. of Papers Published in International Conferences
			-		-	3		1
Books Published/IPRs (Books Details- Title/ISBN/Publisher/Year)			01					
Projects Carried	l Out		-					
		Diploma	Post Diploma	Under Graduate	Post Graduate	G	ost Fraduate Piploma	
Courses Taught					Microprocessor, Integrated Circuits, VLSI Design, VLSI Technology, Digital System Design using Verilog, Embedded System, Fundamentals of Electronics Engineering	Digital Design usin VHDL, Algorithms for VLSI Design Automation		
Patent (Filed/Granted)			1					

Professional Memberships	-
Grants Fetched / Awards	-
Interaction with Professional Institutions	-
Technology Transfer	-